



**LIST OF REFERENCES CITED BY APPLICANT**  
(Use several sheets if necessary)

ATTY. DOCKET NO.:	APPLICATION SERIAL NO.:
85170-4800	10/679,978
APPLICANT:	
Stephen Maxwell Parkes	

FILING DATE: October 6, 2003 GROUP: 2705 2819

**U.S. PATENT DOCUMENTS**

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	PILING DATE IF APPROPRIATE
<u>DC</u>	AA	4,569,041	2/1986	Takeuchi et al.	370	60	
<u>DC</u>	AB	6,072,944	6/2000	Robinson	395	500.05	
	AC						
	AD						

**FOREIGN PATENT DOCUMENTS**

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
<u>DC</u>	AE	EP 0 338 558	10/1989	Europe	—	—	X	
<u>DC</u>	AF	EP 0 867 820	1/1998	Europe	—	—	X	
<u>DC</u>	AG	EP 0 969 631	1/2000	Europe	—	—	X	
<u>DC</u>	AH	EP 1 096 736	5/2001	Europe	—	—	X	
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**OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)**

<u>DC</u>	AK	Parkes, "Space Wire: Links, Nodes, Routers and Networks" ECSS-E-50-12 Space Engineering pp. 1-101, <u>6/2000</u>
<u>DC</u>	AL	Swamy et al. "OO-VHDL, Extensions to VHDL" IEEE (1995)
<u>DC</u>	AM	Guerrier et al. "A Generic Architecture for On-Chip Packet-Switched Interconnections"
<u>DC</u>	AN	Benini et al. "Networks on Chips: A new SoC Paradigm" SOC Designs IEEE (2002)
<u>DC</u>	AO	A. Tsutsui et al., "Special Purpose FPGA for High-speed Digital Telecommunication Systems", NTT Optical Network Systems Laboratories , pp 486-491, IEEE (1995)
<u>DC</u>	AP	D'Angelo et al., "Modular Design of Communication Node Prototypes" pp. 170-175 IEEE (1997)
<u>DC</u>	AQ	Kaptanoglu et al.,(XP-000868474) "A new high density and very low cost reprogrammable FPGA architecture"pp 3-12, (1999)
<u>DC</u>	AR	Vranesic "The FPGA Challenge", Dept. of Electrical and Computer Engineering, pp 121-126 IEEE (1998).

**EXAMINER**

**DATE CONSIDERED**

9/23/04

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

LIST OF REFERENCES CITED BY APPLICANT (Use several sheets if necessary)		ATTY. DOCKET NO.: <b>85170-4800</b>	APPLICATION SERIAL NO.: <b>10679978</b>
		APPLICANT: <b>Stephen Maxwell Parkes</b>	
		FILING DATE:	GROUP:

## U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
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	AC	5,457,410	10/1995	Ting	326	41	
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	AL							

## OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

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